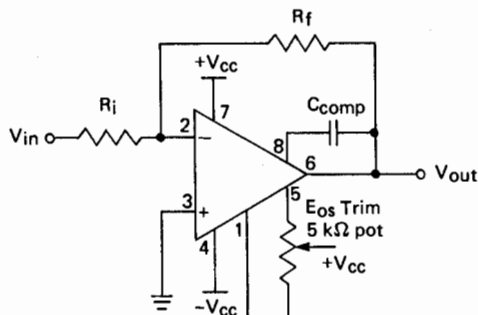


Wideband Fast-Settling Operational Amplifiers

The 1437 and 1438 are designed to offer versatility in wideband steady state and fast transient applications. The absence of large transients and oscillations in the settling waveform makes these op amps dependable system elements that help solve settling problems associated with A/D's, D/A's, and sampling circuits. Among their competitors, the 1437 and 1438 stand out for their speed and predictability, exemplified by their fast and smooth settling.

The 1437 and 1438 have excellent DC characteristics with $\pm 200\text{pA}$ input bias currents, 95dB open loop gains, and $\pm 0.5\text{mV}$ input offset voltages. The choice of a single external compensation capacitor allows for maintenance of a 40MHz bandwidth over a variety of gains, with slew rates up to $400\text{V}/\mu\text{sec}$. A true differential input ensures equally superior performance in all system configurations whether they are inverting, noninverting, or differential. With their attractive price/performance ratios, the 1437 and 1438 should prove to be new industry workhorses in the fields of data acquisition and high-speed, high-accuracy signal processing.

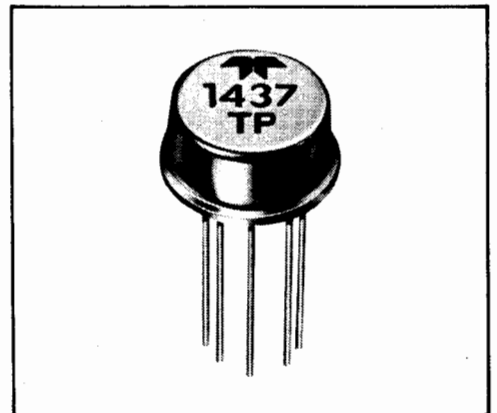
The 1437 is packaged in a TO-99 can and has a guaranteed $\pm 20\text{mA}$ output. The 1438 is packaged in a TO-8 can and has a guaranteed $\pm 50\text{mA}$ output. Standard devices are fully specified for 0°C to $+70^\circ\text{C}$ operation. Add "-83" to part number for -55°C to $+125^\circ\text{C}$ operation and MIL-STD-883, Method 5008 screening.



An E_{OS} trim pot is optional.
Power supplies should be bypassed with $1\ \mu\text{F}$ tantalum and $0.01\ \mu\text{F}$ ceramic capacitors.
The use of a finned heat sink is recommended.

Figure 1. Normal Inverting Operation

1437 1438



FEATURES

- 350MHz Gain-Bandwidth Product
- FET Input
- 40MHz Operating Bandwidth
- 110ns Settling Time to 0.1%
- $\pm 20\text{mA}$ and $\pm 50\text{mA}$ Outputs
- Small TO-99 and TO-8 Packages
- Single External Compensation Capacitor

APPLICATIONS

- Current to Voltage DAC's
- Pulse Amplifiers
- Radar and Sonar Signal Processing
- Graphic CRT Displays
- Video A/D, D/A, and S/H's

SPECIFICATIONS (+25°C; $V_{CC} = \pm 15V$; $R_L = 500\Omega$; $R_f, R_i = 2k$, unless otherwise indicated). ①

	Typical	Guaranteed
OUTPUT RANGE Voltage (peak) ① Current ① ②	$\pm 12V$ $\pm 24mA$	$\pm 10V$ $\pm 20mA$
VOLTAGE GAIN (open loop @ $f = 10Hz$) ① ③ Rated Load	95dB	88dB
FREQUENCY RESPONSE Gain-Bandwidth Product ① Unity-Gain Bandwidth Full Power Bandwidth $C_c = 0pF$ $C_c = 15pF$	350MHz 40MHz 6.0MHz 3.1MHz	--- --- --- ---
TIME RESPONSE Settling Time ① 10V Step to 1% 10V Step to 0.1% ③ 10V Step to 0.025% Small Signal Rise Time $C_c = 15pF$ Slew Rate $C_c = 0$ $C_c = 15pF$	85ns 110ns 150ns 9ns 400V/ μsec 225V/ μsec	--- 140ns --- --- ---
INPUT VOLTAGE RANGE/CMRR/IMPEDANCE Common Mode for DC Linear Operation ① Absolute Max. Differential Between Inputs CMRR @ DC ① ③ PSRR @ DC Input Impedance @ DC (differential) Output Impedance (open loop)	$\pm 12V$ --- 78dB 76dB 10M Ω 3pF 90 Ω	$\pm 10V$ $\pm 20V$ 60dB --- --- ---
INPUT OFFSET VOLTAGE Initial (without external trim) ① Vs. Temperature ($R_L = \infty$) ①	$\pm 0.5mV$ $\pm 15\mu V/^\circ C$	$\pm 2mV$ $\pm 50\mu V/^\circ C$
INPUT BIAS CURRENT Initial (without external trim) Bias Current over Rated Temperature Offset Current	$\pm 200pA$ Doubles every 10 $^\circ C$ $\pm 20pA$	--- --- ---
NOISE (referred to input) 0.1Hz to 100Hz 100Hz to 10kHz 10kHz to 1MHz	4 μV (p-p) 0.5 μV RMS 5 μV (p-p) 1 μV RMS 50 μV (p-p) 6 μV RMS	--- --- --- --- --- ---
POWER REQUIREMENTS Nominal Power Supply Voltage Supply Voltage Range Quiescent Current @ $V_{CC} = \pm 15V$ ① ③ Short Circuit Current ① ③ ④	$\pm 15V$ --- $\pm 12mA$ 50mA	--- $\pm 12V$ to $\pm 20V$ $\pm 15mA$ ---
TEMPERATURE RANGE Operating 1437 1437-83 Thermal Resistance (θ_{JA}) (θ_{JC}) Storage All Units	--- --- 150 $^\circ C/Watt$ 70 $^\circ C/Watt$ ---	0 $^\circ C$ to +70 $^\circ C$ -55 $^\circ C$ to +125 $^\circ C$ --- -65 $^\circ C$ to +150 $^\circ C$

- ① Measured at 10MHz. $C_c = 0$.
 ② Settling time measured in circuit of Figure 7.
 ③ See Figure 13 for plot of CMRR vs. frequency.
 ④ 100% interim tested for "-83" version.
 ⑤ 100% final electrical tested for "-83" version.
 ⑥ Finned heat sink use recommended if long term short circuits can occur. Without finned heat sink, short-circuited duration should be limited to < 10s.
 ⑦ Specifications listed are for the 1437 only. At the time of this printing, the 1438 ($\pm 50mA$ guaranteed output), was still being characterized. Its DC characteristics (offset, bias, CMRR, etc.) will be the same as the 1437's. Its time and frequency response characteristics (slew rate, settling time, etc.) will be different. If a 1438 data sheet is not attached, please contact the factory for a complete specification.

Applications

Its fast settling characteristics make the 1437 an excellent buffer for successive approximation A/D converters. See Figure 2.

With an inverting gain of 10, bandwidth is 30 MHz. See Figure 5.

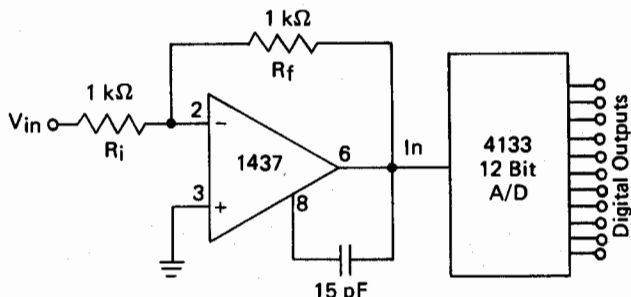


Figure 2. Fast-Settling Buffer

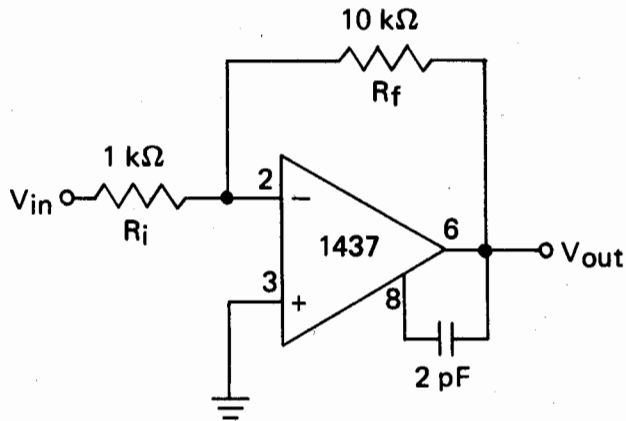


Figure 5. Inverting Gain of 10

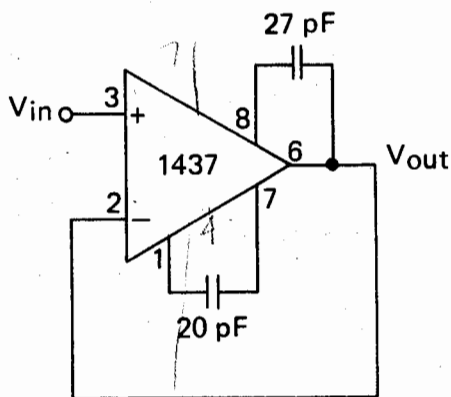


Figure 3. 40MHz Unity Gain Buffer

There is no need for a compensation capacitor for gains over 30. See Figure 4 where gain is 70 and bandwidth is 5 MHz.

This current to voltage converter settles in less than 400 ns to 0.025%. See Figure 6.

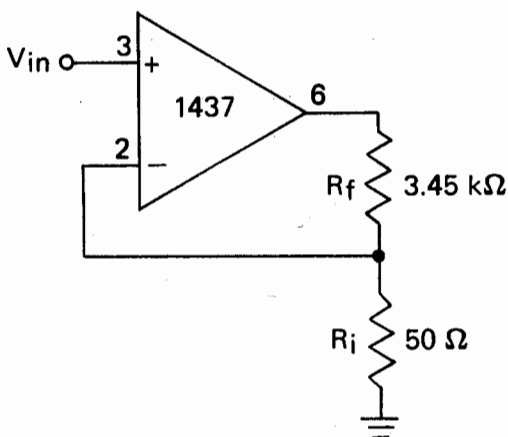


Figure 4. Video Amplifier

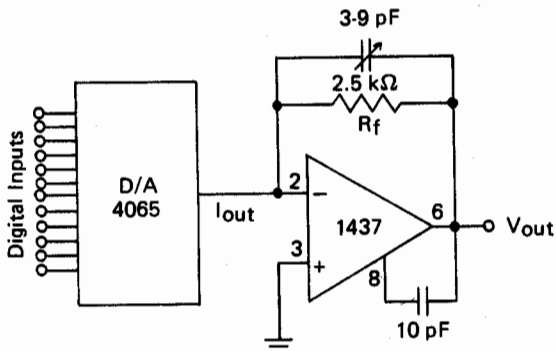


Figure 6. Current to Voltage Converter

Typical Performance Curves

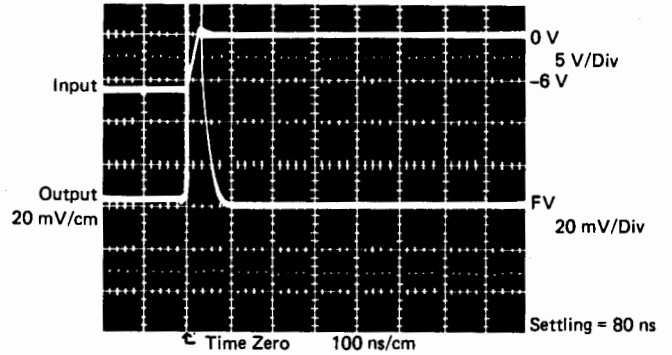
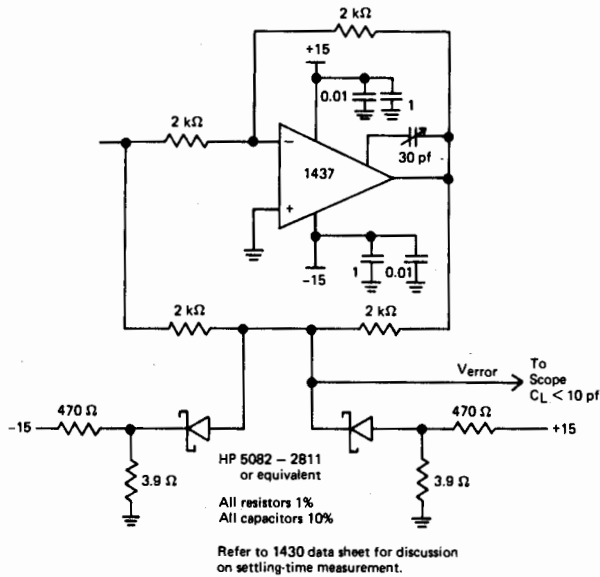


Figure 7. Settling Time Test Circuit and Graph

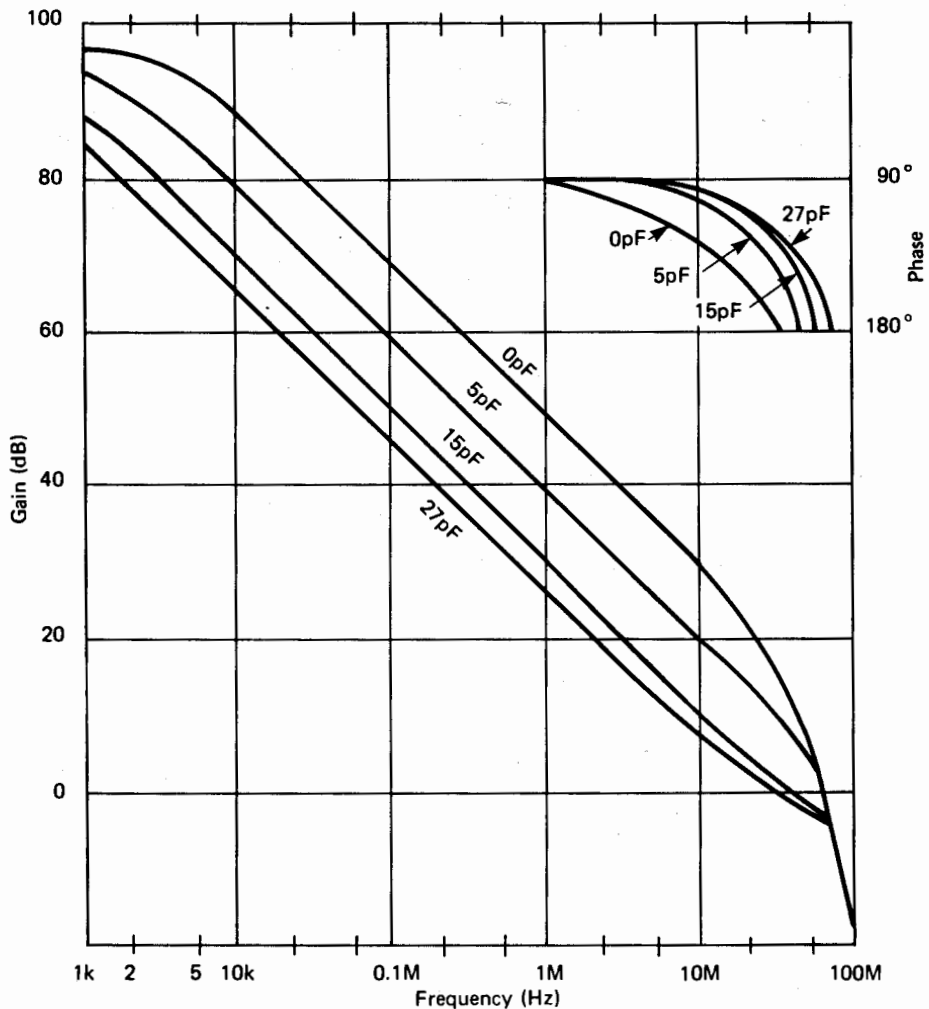


Figure 8. Open Loop Gain and Phase vs. Frequency

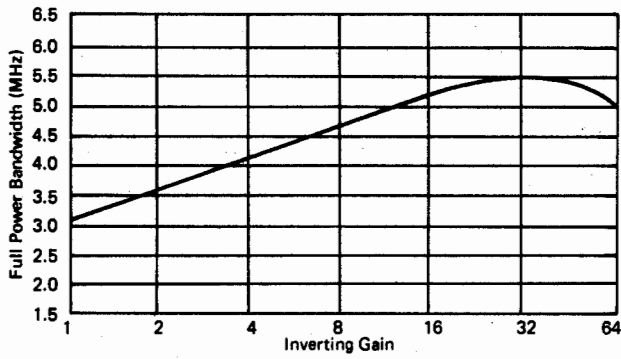


Figure 9. Full Power Bandwidth vs. Inverting Gain

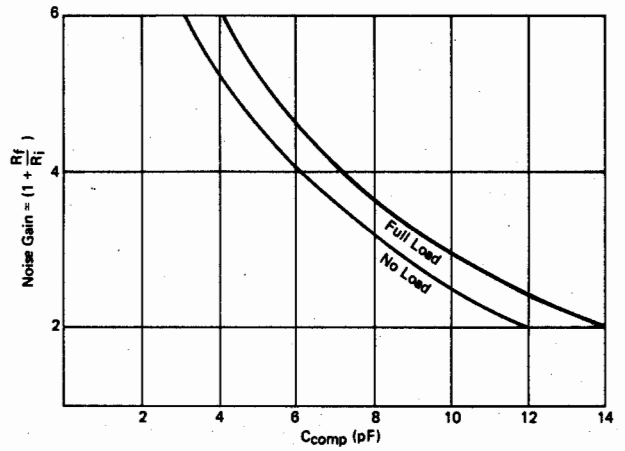


Figure 12. Noise Gain vs. Ccomp for 18% Overshoot

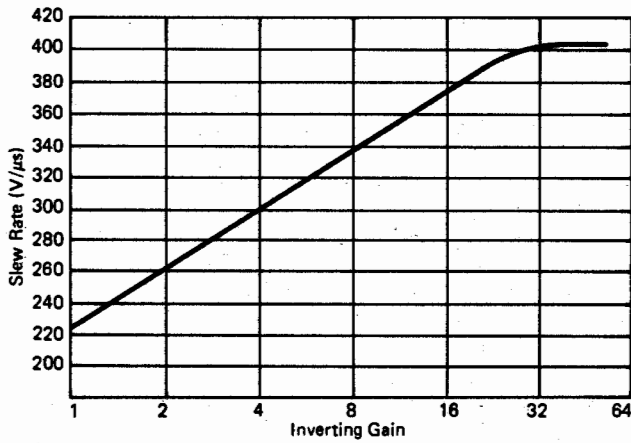


Figure 10. Slew Rate vs. Inverting Gain

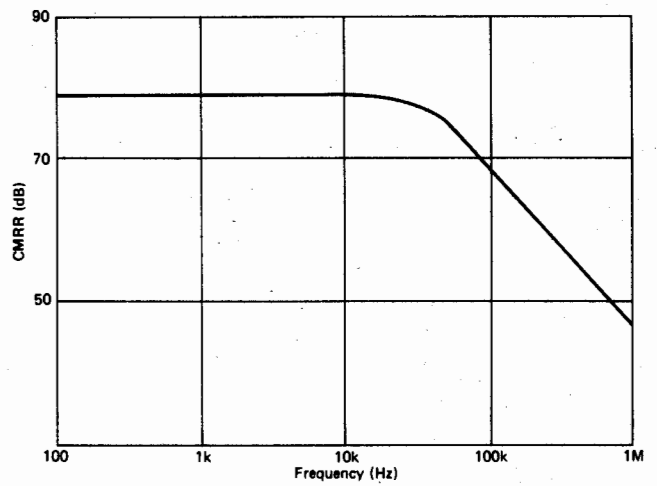


Figure 13. CMRR vs. Frequency

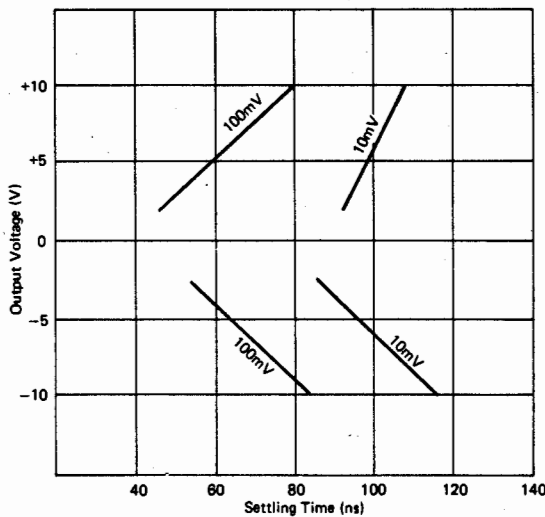


Figure 11. Settling Time vs. Output Voltage Change

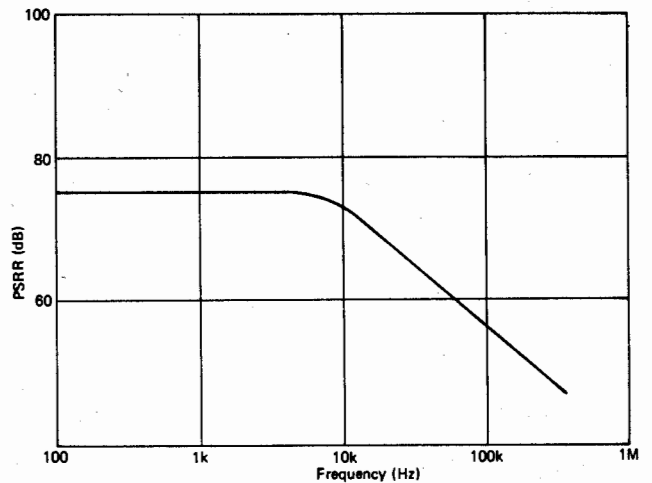


Figure 14. PSRR vs. Frequency, ΔPS = 1%

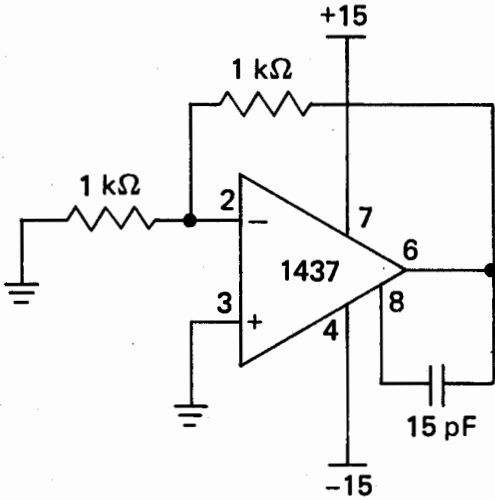
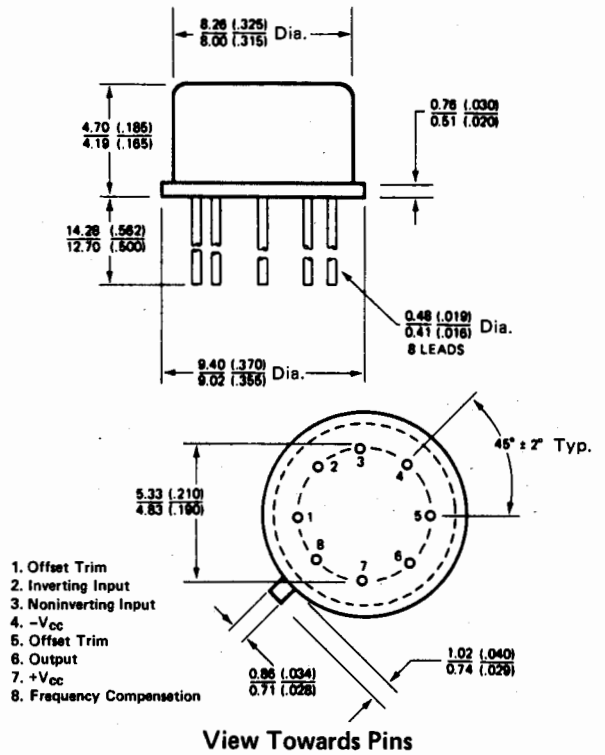


Figure 15. Burn-In Test Circuit



- 1. Offset Trim
- 2. Inverting Input
- 3. Noninverting Input
- 4. -Vcc
- 5. Offset Trim
- 6. Output
- 7. +Vcc
- 8. Frequency Compensation

View Towards Pins

TO-99

Dimensions are in millimeters and those in parentheses in inches.

Not shown actual size.

Figure 16. Pinouts

SCREENING ACCORDING TO MIL-STD-883 METHOD 5008		
Test	Methods and Conditions	Purpose
① Internal Visual	Method 2017	Removes potentially defective units with respect to materials, construction, and workmanship.
① Stabilization Bake	Method 1008, Condition C 24 hours at 150 °C	Stabilizes circuit components, with a preconditioning treatment, prior to conducting further testing and trimming.
① Constant Acceleration	Method 2001, Condition B Y ₁ Axis, 10,000 g	Removes potential failures due to weak wire or chip bonding.
① Seal, Fine and Gross	Method 1014, Fine Leak Condition A and C	Verifies Integrity of hermetic package.
Interim Electrical	----	----
② Burn In	Method 1015, Condition C 160 hours at 125 °C	Reduces infant mortality rate.
Temperature Cycling	Method 1010, Condition B 10 cycles from -65 °C +0 °C to +150 °C +3 °C -5 °C -0 °C	Removes potential failures due to weak wire or chip bonding.
Final Electrical	----	----
① External Visual	Method 2009	Removes defective units with respect to materials, construction, and workmanship.

① Standard tests for all 1437 units.

② See Figure 16 for burn-in circuit used.

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